

Dead-time effect in current-controlled three-phase PWM inverter causes the low order harmonics in output current. Instead of using real inductor, a virtual inductor is proposed to minimize dead ...

The switching lag-time, which prevents the phase shortage of inverter arms, causes serious distortions of the output voltage of the inverter. This effect is well known as the dead-time ...

The reverse conduction capability of MOSFETs is beneficial for the efficiency of a three-phase inverter. In this article, analytical expressions in closed form are presented which ...

In this conduction mode of three phase inverter, each thyristor conducts for 180° . Thyristor pair in each arm i.e. (T1, T4), (T3, T6) and (T5, T2) are turned on with a time interval of 180° . It ...

Three-phase inverter on time

